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12-17-04	(Date)

12/20/2004 SDIRETA2 00000063 200668 10634008

01 FC:1501 1400.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/634,008	08/04/2003	Chris D. Atkinson	TI-31132.1	5044

TITLE OF INVENTION: ERROR REDUCTION IN SEMICONDUCTOR PROCESSES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
Nonprovisional	NO	\$1400	\$300	\$1700	12/27/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
Chung, Phung M	2133	714-724000

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1. Yingsheng Tung
2. Wade James Brady III
3. Frederick J. Telecky, Jr.

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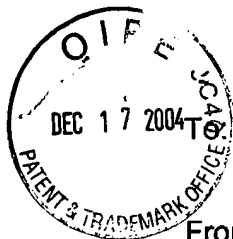
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

**Atkinson et al.**

Docket Number: **TI-31132.1**

Serial No.: **10/634,008**

Art Unit: **2133**

Filed: **08/04/2003**

Examiner: **P. Chung**

For: **ERROR REDUCTION IN SEMICONDUCTOR PROCESSES**

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<input type="checkbox"/> <b>RCE TRANSMITTAL</b>			
<b>NAME OF INVENTOR(S):</b> <b>Atkinson et al.</b>		<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 10/634,008</b>	
<b>TITLE OF INVENTION:</b> <b>Error Reduction in Semiconductor Processes</b>			
<b>TI FILE NO.:</b> <b>TI-31132.1</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>		
<b>FAXED: 12/17/2004</b>			
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